

Date: Wednesday, March 29

Offering: Winter 2006

This assignment is due at the beginning of class, Wednesday, April 5. If you have any questions, see the instructor or the teaching assistant. **Make sure to show all of your work in obtaining solutions.** Simply writing down the solution is *not* enough. Note: Multiple answer sheets *must* be securely stapled. Loose sheets will *not* be accepted. Please print your name on the top of each sheet, along with your student ID.

1. Find a minimized state table for the synchronous sequential circuit below using an implication table.

	I	J
A	B/0	A/1
B	C/0	A/0
C	C/0	B/0
D	E/0	D/1
E	C/0	D/0

2. Please answer the following questions from Chapter 7 of your textbook.

- 7-1
- 7-2
- 7-3
- 7-7
- 7-8